

History of Switching Theory

Element	Two-level	Network Multi-level	Sequential	Language & System
1847	Boole			
1937	Nakashima			
1938	Shannon			
1941		Post		
1947	Transistor			
1949	Shannon	Shannon		
1952	Quine			
1953	Karnaugh			
1954	Parametron		Huffman	
1956	Muller McCluskey			FORTRAN
1957	IC	Ashenurst Lupanov	Mealy	
1958		Markov		
1960		Roth	Moore	ALGOL/LISP
1962		Curtis		
1964	SSI	Lawler		
1965	Miller, Harrison	TANT		
1967	Tison	NAND Synthesis		DDL
1968				
1969	Svoboda			ALERT
1970	PLA	Integer Programming	Unger	
1971	4004	Dietmeyer		
1974	8080	MINI		
1978	8086	Davio		
1979		Muroga		
1980		LSS		
1981			Retiming	
1982	Brayton	Weak division		IDL
1984	68020	ESPRESSO-II MINI-II		VHDL
1985	FPGA	ESPRESSO-MV		SOCRATES PARTHENON
1986		Bryant BDD		CONES
1987		Applications of BDDs		MIS
1989	Boolean Relation			
1992	EXMIN2			