On Properties of Kleene TDDs

Yukihiro IGUCHI
Dept. of Computer Science
Meiji University
Kawasaki 214-71, Japan
e-mail: iguchi@cs.meiji.ac.jp

Tsutomu SASAO Munehiro MATSUURA
Dept. of Computer Science and Electronics
Kyushu Institute of Technology
Iizuka 820, Japan

sasao@cse.kyutech.ac.jp matsuura@cse.kyutech.ac.jp

Abstract— Three types of ternary decision diagrams (TDDs) are considered: AND_TDDs, EXOR_TDDs, and Kleene_TDDs. Kleene_TDDs are useful for logic simulation in the presence of unknown inputs. Let N(BDD:f), $N(AND_TDD:f)$, and $N(EXOR_TDD:f)$ be the number of non-terminal nodes in the BDD, the AND_TDD, and the EXOR_TDD for f, respectively. Let $N(Kleene_TDD:\mathcal{F})$ be the number of non-terminal nodes in the Kleene_TDD for \mathcal{F} , where \mathcal{F} is the Kleenean ternary function corresponding to f. Then $N(BDD:f) \leq N(TDD:f)$. For parity functions, $N(BDD:f) = N(AND_TDD:f) = N(EXOR_TDD:f) = N(Kleene_TDD:\mathcal{F})$. For unate functions, $N(BDD:f) = N(AND_TDD:f)$ and $O(n^3)$ for arbitrary functions, and symmetric functions, respectively. There exist a 2n-variable function, where Kleene_TDDs require O(n) nodes with the best order, while $O(3^n)$ nodes in the worst order.

I. Introduction

Various methods exist for representing logic functions. A truth table is the most straightforword method. Another method is a sum-of-products expression (SOP). Binary Decision Diagrams (BDDs)[2] are commonly used in logic synthesis[8, 9], since they can represent complex functions with many variables. Recently, Ternary Decision Diagrams (TDDs) have been developed as an alternative representation of logic functions[10]. TDDs are similar to BDDs, except that each non-terminal node has three children. Some TDDs have terminals other than constant 0 and 1. In this paper, we consider three types of TDDs: AND_TDDs, EXOR_TDDs, and Kleene_TDDs. AND_TDDs represent the sets of the implicants implicitly[11]. They can treat functions for which the conventional cube-based method[4] fails. EXOR_TDDs are useful to minimize AND-EXOR logic expressions[12]. The TDD presented by Jennings (hereafter, we will call it Kleene_TDD) is useful to evaluate logical functions in the presence of unknown inputs[5].

In this paper, we will show some properties of Kleene_TDDs.

II. EVALUATION OF LOGIC FUNCTIONS IN THE PRESENCE OF UNKNOWN INPUTS

Let $B = \{0,1\}$. An *n*-variable switching function f represents the mapping: $f: B^n \to B$. Let $a = (a_1, a_2, \ldots, a_n)$ be a binary vector, where $a_i \in B$. We often have to evaluate the value f(a) for a, where some a_i are unknown[1]. When we do logic simulation for sequential circuits, we have to consider such inputs. In this section, we will review the method to evaluate f in the presence of unknown inputs.

Let $T = \{0, 1, u\}$, where u is the truth value showing an unknown input. Let $\alpha = (\alpha_1, \alpha_2, \dots, \alpha_n)$ be a ternary vector, where $\alpha_i \in T$. If α_i is either 0 or 1 for all i, then $\alpha \in B^n$. In this case $f(\alpha)$ is either 0 or 1. If $\alpha_i = u$

X	0	1	и	X	0	1	и	×	0	1	и	X	0	1	и	X	0	1	u	
Ó	0	0	0	Ó	0	1	и	ó	0	1	u		1	0	u	Ó	0	и	u	
1	0	1	и	1	1	1	1	1	1	0	и		N	тс	×	1	и	1	u	
и	0	и	и	u	и	1	и	u	и	u	и		10			u	и	и	u	
	A٨	ID	х•у		OR	х	Vу	Е	XC	R	х⊕у					Alig	gnn	nen	t xo	У

Fig. 2.1. Ternary AND, OR, NOT, EXOR, and Alignment operations

for some i, then $\alpha \in T^n - B^n$. In this case, for some α , $f(\alpha)$ is either 0 or 1, but for other α , $f(\alpha)$ cannot be determined. Therefore, it is convenient to introduce a three-valued logic function, $\mathcal{F}: T^n \to T$, which is derived from f. Note that f uniquely defines \mathcal{F} .

Definition 2.1 Let $\alpha \in T^n$. $A(\alpha)$ denotes the set of all the binary vectors that are obtained by replacing all u with 0 or 1.

Let k be the number of u's in α , then the set $A(\alpha)$ consists of 2^k binary vectors.

Definition 2.2 Let f be a two-valued logic function, and $\alpha \in T^n$.

$$f(A(\alpha)) = \{f(a) \mid a \in A(\alpha)\}.$$

$$\mathcal{F}(\alpha) = \begin{cases} 0 & \text{if } f(A(\alpha)) = \{0\}\\ 1 & \text{if } f(A(\alpha)) = \{1\}\\ u & \text{if. } f(A(\alpha)) = \{0, 1\} \end{cases}$$

Example 2.1 Consider the expression $f(x_1, x_2, x_3) = x_1\bar{x}_2 \vee x_2x_3$. For the inputs $\alpha_1 = (0, 0, u)$, $\alpha_2 = (1, u, 1)$, and $\alpha_3 = (1, u, u)$, $\mathcal{F}(\alpha_1)$, $\mathcal{F}(\alpha_2)$, and $\mathcal{F}(\alpha_3)$ are derived as follows:

$$\begin{array}{l} f(A(\boldsymbol{\alpha}_1)) \! = \! \{f(0,0,0),f(0,0,1)\} = \{0\}, \\ f(A(\boldsymbol{\alpha}_2)) \! = \! \{f(1,0,1),f(1,1,1)\} = \{1\}, \quad and \\ f(A(\boldsymbol{\alpha}_3)) \! = \! \{f(1,0,0),f(1,0,1),f(1,1,0),f(1,1,1)\} = \{0,1\}. \\ By \ Definition \ 2.2, \ we \ have \end{array}$$

$$\mathcal{F}(\boldsymbol{\alpha}_1) = 0$$
, $\mathcal{F}(\boldsymbol{\alpha}_2) = 1$, and $\mathcal{F}(\boldsymbol{\alpha}_3) = u$.

When we do gate-level logic simulation, we extend binary operations to ternary logic as shown in Fig.2.1. This is the Kleenean strong ternary logic[6]. In this case, signals are evaluated from the primary inputs to the primary outputs by using Fig.2.1.

Example 2.2 Fig. 2.2 shows an AND-OR network that realizes the expression in Example 2.1. When we evaluate the output f for the input $\alpha_2 = (1, u, 1)$ by using a naive method, we have the output u as shown in Fig.2.2. However, Example 2.1 shows that

$$\mathcal{F}(\boldsymbol{\alpha}_2) = 1.$$

Thus, the naive method does not always produce accurate results.

Several methods exist to evaluate output values according to Definition 2.2: including representations using SOPs, using BDDs[3], and using Kleene_TDDs[5].

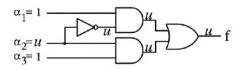


Fig. 2.2. A naive method to evaluate the function

III. BDDs and TDDs

In this section, we will give formal definitions for BDDs and TDDs.

Definition 3.1 A BDD is a rooted, directed graph with node set V containing two types of nodes:

A non-terminal node v has as attributes an argument index index $(v) \in \{1, ..., n\}$, and two children $low(v), high(v) \in V$. A terminal node v has as attribute a value $value(v) \in B$.

For any non-terminal node v, if low(v) is also nonterminal, then index(v) < index(low(v)). Similarly, if high(v) is also non-terminal, then index(v)index(high(v)). The correspondence between BDDs and Boolean functions is defined as follows:

For a terminal node v:

$$\begin{array}{ll} If & value(v) = 1, & then \ f_v = 1. \\ If & value(v) = 0, & then \ f_v = 0. \end{array}$$

For a non-terminal node v:

Let
$$X = (x_1, x_2, \dots, x_{i-1}, x_{i+1}, \dots, x_n).$$

If
$$index(v) = i$$
, then f_v is a function such that

$$f_v(x_1, \dots, x_n) = \bar{x}_i \cdot f_{low(v)}(X) \lor x_i \cdot f_{high(v)}(X).$$

Note that the root node represents the function f itself.

Definition 3.2 A BDD is a Reduced Ordered Binary Decision Diagram (ROBDD) if it contains no node v with low(v) = high(v), and if it does not contain distinct nodes v_1 and v_2 such that the subgraph rooted by v_1 and v_2 are equivalent.

Example 3.1 Fig. 3.1(a) shows the ROBDD for the function in Example 2.1. The number $\theta(1)$ attached to each edge incident to v denotes low(v)(high(v)).

TDDs are similar to BDDs, except that each nonterminal node v has three children, low(v), high(v), and middle(v).

Definition 3.3 A TDD is a rooted, directed graph with node set V containing two types of nodes:

A non-terminal node v has as attributes an argument index index $(v) \in \{1, \ldots, n\}$, and three children low(v), high(v), and $middle(v) \in V$. A terminal node v has as attribute a value $value(v) \in T$. For any non-terminal node v, if low(v) is also non-terminal, then index(v) < vindex(low(v)). Similarly, if high(v) is non-terminal, then index(v) < index(high(v)). Also, if middle(v) is nonterminal, then index(v) < index(middle(v)).

Definition 3.4 A TDD is a Reduced Ordered Ternary Decision Diagram(ROTDD) if it contains no node v with low(v) = high(v), and if it does not contain distinct nodes v_1 and v_2 such that the subgraph rooted by v_1 and v_2 are equivalent.

Different assignments of operations to the third child produce different TDDs.

Definition 3.5 Fig. 2.1 shows the ternary operation Alignment. Let $a, b \in T$.

$$a\odot b=\left\{\begin{matrix} a & \textit{if} & a=b\\ u & \textit{otherwise} \end{matrix}\right.$$

The correspondence between AND_TDD (EXOR_TDD, Kleene_TDD) and a function f is defined as follows: For terminal node v:

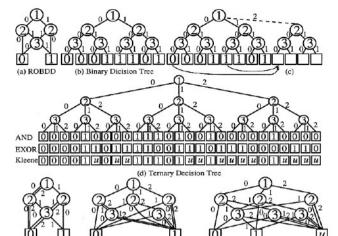


Fig. 3.1. BDD and TDDs

If
$$value(v) = 1$$
, then $f_v = 1$.
If $value(v) = 0$, then $f_v = 0$.

 $\begin{array}{c} If \ \ value(v)=1, \ \ then \ f_v=1. \\ If \ \ value(v)=0, \ \ then \ f_v=0. \\ For \ non-terminal \ node \ v: \ If \ index(v)=i, \ then \ f_v \ and \end{array}$ $f_{middle(v)}$ are the functions such that $f_v(\mathbf{X}) = \bar{x}_i \cdot f_{low(v)}(\mathbf{X}) \vee x_i \cdot f_{high(v)}(\mathbf{X}).$

(g) Kleene_TDD

In the case of an AND_TDD:

(f) EXOR_TDD

$$f_{middle(v)}(X) = f_{low(v)}(X) \cdot f_{high(v)}(X).$$
• In the case of an EXOR_TDD:

$$f_{middle(v)}(oldsymbol{X}) = f_{low(v)}(oldsymbol{X}) \oplus f_{hogh}(oldsymbol{X}).$$
• In the case of a Kleene_TDD:

$$f_{middle(v)}(X) = f_{low(v)}(X) \odot f_{high(v)}(X).$$

If the above relation does not hold, then there is no function for the TDD.

Example 3.2 Fig. 3.1 shows the AND_TDD, the $EXOR_TDD$, and the Kleene_TDD for f and $\mathcal F$ in Example 2.1. By expanding the ROBDD in (a), we have the complete binary decision tree (b). In Fig. 3.1(c), the new edge at the root node is generated, and the subgraph is created by using the AND operation to derive the AND_TDD. In other words, f_{200} is obtained as the AND of f_{000} and f_{100} . When EXOR is used to generate the middle child, we have an EXOR_TDD. When Alignment is used to generate the middle child, we have a Kleene_TDD. Here, 0, 1, and 2 attached to each edge denote low(v), high(v), and middle(v), respectively. For each subsequent node, create the third node recursively down to the leaves, and we have the complete ternary decision tree shown in (d). By eliminating all the redundant nodes, and sharing all the equivalent sub-graphs, we obtain an AND_TDD, an $EXOR_TDD$, and a Kleene_TDD as shown in (e), (f), and (g), respectively.

 AND_TDDs represent the set of implicants implicitly[11]. EXOR_TDDs are useful to minimize AND-EXOR expressions[12]. Kleene_TDDs are useful to evaluate logic functions in the presence of unknown inputs[5].

Example 3.3 We can evaluate $\mathcal{F}(\alpha)$ in Example 2.1 by using the Kleene_TDD in Fig.3.1(g). $\mathcal{F}(\alpha)$ is obatained by tracing the edges from the root node to a terminal node according to the value of α . When the input is $\alpha_1 = (0,0,u)$, trace the edges, 0, 0, and 2, and reach the terminal node 0. Thus, we have $\mathcal{F}(0,0,u)=0$. Similarly, when the input is $\alpha_2 = (1, u, 1)$, trace the edge, 1, 2, 1, and reach the terminal node 1. Thus, $\mathcal{F}(1, u, 1) = 1$. When the input is $\alpha_3 = (1, u, u)$, by tracing 1,2,2, and reach the terminal node u. Thus, we have $\mathcal{F}(1, u, u) = u$.

IV. Complexities of TDDs

Definition 4.1 Let N(BDD:f), $N(AND_TDD:f)$, and $N(EXOR_TDD:f)$ be the number of non-terminal nodes in the BDD, the AND_TDD, and the EXOR_TDD for f, respectively. Let $N(Kleene_TDD:\mathcal{F})$ be the numbers of non-terminal nodes in the $Kleene_TDD$ for \mathcal{F} .

Theorem 4.1

 $\begin{array}{l} N(BDD:f) \leq N(AND_TDD:f), \\ N(BDD:f) \leq N(EXOR_TDD:f), \quad and \\ N(BDD:f) \leq N(Kleene_TDD:\mathcal{F}). \end{array}$

Theorem 4.2

$$N(AND_TDD: f) \leq N(Kleene_TDD: \mathcal{F}).$$

Theorem 4.3 Let f be a parity function. Then, we have $N(BDD:f) = N(AND_TDD:f) = N(f:EXOR_TDD) = N(Kleene_TDD:\mathcal{F}).$

Theorem 4.4 Let f be a unate function. Then, $N(BDD:f) = N(AND_TDD:f)$.

The number of nodes in a complete ternary decision tree for an *n*-variable function is:

$$1 + 3^1 + 3^2 + \ldots + 3^n = (3^{n+1} - 1)/2.$$

However, in a reduced TDD, only one sub-graph is realized for each sub-function. Thus, the number of nodes can be reduced.

We can state the following:

Lemma 4.1 All the functions of k or fewer variables can be represented by an Kleene_TDD with at most 3^{3^k} nodes. Theorem 4.5 An arbitrary n-variable function can be represented by a Kleene_TDD with at most

$$\min_{k=1}^{n} \left(\frac{3^{k+1}-1}{2} + 3^{3^{n-k}} \right)$$

nodes.

Corollary 4.1 An arbitrary n-variable function can be represented by a Kleene_TDD with $O(3^n/n)$ nodes.

Theorem 4.6 An arbitrary symmetric function of n-variables can be represented by a Kleene_TDD with $O(n^3)$ nodes.

Theorem 4.7

 $N(BDD:f) = N(BDD:\bar{f}).$ $N(EXOR_TDD:f) = N(EXOR_TDD:\bar{f}).$ $N(Kleene_TDD:f) = N(Kleene_TDD:\bar{f}).$ However, in general, $N(AND_TDD:f) \neq N(AND_TDD:\bar{f}).$

V. Experiments and Observation

A. Sizes of TDDs

We developed TDD algorithms, and generated TDDs for various benchmark functions. Multiple-output functions are represented by shared BDDs and shared TDDs. Table 5.1 compares the number of non-terminal nodes in BDDs, AND_TDDs, EXOR_TDDs, and Kleene_TDDs. From this table, we observe the following:

1. Except for e64, rd84, t481, xor5, and z5xp1, $N(AND_TDD: f) < N(EXOR_TDD: f)$ holds.

2. TDDs are considerably larger than corresponding BDDs.

3. $\operatorname{xor5}$ is a parity function. Thus, $N(BDD:f) = N(AND \cdot TDD:f) = N(EXOR \cdot TDD:f) = N(Kleene \cdot TDD:F).$

Table 5.2 shows the sizes of Kleene_TDDs for randomly generated functions. We can observe that the sizes of Kleene_TDDs are $O(3^n/n)$ for n-variable randomly generated functions.

Table 5.2 also shows the sizes of Kleene_TDDs for the Achilles' heel function: $f = x_1y_1 \lor x_2y_2 \lor \ldots \lor x_ny_n$. The size is $O(3^n)$ when the variable order is $x_1, x_2, \ldots, x_n, y_1, y_2, \ldots, y_n$, while 5n-2 when the variable order is $x_1, y_1, x_2, y_2, \ldots, x_n, y_n$. This shows that the sizes of TDDs greatly depends on the variable ordering.

TABLE 5.1

Sizes of BDDs and TDDs						
function	in	out	BDD			
				AND	EXOR	Kleene
9sym apex1 apex2 apex3 apex5 cordic cps duke2 e64 ex5 misex1 misex2 misex3 pdc rd84 sao2 seq spla t481 vg2 xor5	$\begin{array}{c} 9\\45\\39\\54\\123\\222\\65\\8\\8\\25\\14\\16\\41\\16\\25\\57\end{array}$	1 45 3 50 88 2 109 65 63 7 18 44 4 4 35 46 1	33 1332 410 935 1078 75 987 336 1379 278 36 81 542 560 59 81 32 1948	60 6249 542 3161 3039 83 1457 522 1379 381 1219 1024 79 1143 873 717 48 3873 717	70 47814 3575 34574 3282 153 4808 2176 1379 444 2321 72 216 67414 2237 43 865 9	94 18401 35000 7119 4204 271 5653 2555 2693 628 92 204 3262 3031 121 305 18745 2315 6961
z5xp1	- 1	10	68	79	75	158

TABLE 5.2
Sizes of Kleene_TDDs for n-variable randamly generated functions and Achilles' heel functions

n	Random	$ f = x_1 y_1 \vee \ldots \vee x_n y_n $						
	function	worst	optimum					
3 4 5 6 7 8 9 10 11 12 13	28 638 139 295 592 1357 2898 6165 12905	42 127 378 1123 3342 9967 29778 89083 266742 799207 2395578	13 18 23 23 33 38 43 48 53 58					

B. Dependency on the number of true minterms

We generated pseudo-random logic functions of n=14 variables for different number of true minterms $s(0 < s < 2^n)$, and counted numbers of nodes in BDDs and TDDs. Fig. 5.1(a) shows the numbers of BDD nodes for different s. As shown in Theorem 4.7, the graph is symmetric with respect to $s=2^{n-1}$. Also, the number of nodes takes its maximum when $s=2^{n-1}$. Fig. 5.1(b) shows the number of TDD nodes. As shown in Theorem 4.7 the graphs for EXOR_TDDs and Kleene_TDDs, are symmetric with respect to $s=2^{n-1}$. However, the graph for AND_TDDs is asymmetric. Surprisingly, the number of

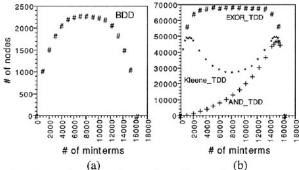


Fig. 5.1. Dependency on the number of true minterms

nodes in Kleene_TDDs takes its maximum for two values of s, and takes its local minimum for $s = 2^{n-1}$. This is quite different from the case of BDDs.

C. CPU time

Although Kleene_TDDs are greater than BDDs, Kleene_TDD-based logic simulation are more efficient than BDD-based one.

In logic simulation for design verification, expected outputs for networks are 1 or 0, rather than u. For a given logic function, input vectors can be generated as follows:

To verify the ON set.

Let F be an irredundant sum-of-products expression (ISOP) for f. For each product p_j of F, we have a corresponding input vector α such that $f(\alpha) = 1$ as follows:

$$\alpha = (\alpha_1, \alpha_2, \dots, \alpha_n), \text{ where }$$

$$\alpha_i = \begin{cases} 0 : p_j \text{ contains the literal } \bar{x}_i, \\ 1 : p_j \text{ contains the literal } x_i, \\ u : \text{ otherwise.} \end{cases}$$

To verify the OFF set.

Let \bar{F} be an ISOP for \bar{f} .

For each product q_j of \bar{F} , we have a corresponding input vector $\boldsymbol{\beta}$ such that $f(\boldsymbol{\beta}) = 0$ as follows:

$$\beta = (\beta_1, \beta_2, \dots, \beta_n), \text{ where } \\ \beta_i = \begin{cases} 0: q_j \text{ contains the literal } \bar{x}_i, \\ 1: q_j \text{ contains the literal } x_i, \\ u: \text{ otherwise.} \end{cases}$$

Note that the total number of input vectors is equal to the sum of products in F and \overline{F} .

Fig.5.2 compares simulation time for adders (adr6~ adr12) for the vecters generated in the above method. This figure shows that Kleene_TDD-based simulation is faster than BDD based one.

D. Observation

The facts in 5.B can be interpreted as follows:

 In AND_TDDs, 1-paths correspond to the implicants of f. The average number of implicants of n-variable functions with s true minterms is given by [7].

$$G(n,s) = \sum_{k=0}^{n} 2^{n-k} \binom{n}{k} \frac{\binom{w-w_k}{s-w_k}}{\binom{w}{s}},$$
where $w = 2^n$, and $w_k = 2^k$.

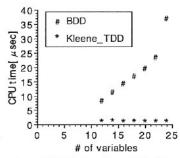


Fig. 5.2. Comparison of CPU time for adr6~12

G(n, s) takes its maximum when s is near to 2^n . Suppose that the number of nodes in the TDD is monotone increasing with the number of paths. Then the graph for AND_TDD has similar shape as the graph for G(n, s).

2. In a Kleene_TDD, 1-paths correspond to the implicants of f, and 0-paths correspond to the implicants of \bar{f} . Thus, a Kleene_TDD denotes implicants of f and \bar{f} at the same time. The average number of implicants for f and \bar{f} is given by

$$G(n,s) + G(n,2^n - s).$$

Thus, the shape of the graph is symmetric with respect to $s = 2^{n-1}$, and the graph takes its maximum for two values of s.

ACKNOWLEDGEMENTS

This work was supported in part by a Grant in Aid for Scientific Research of the Ministry of Education, Science, Culture and Sports of Japan, and On-Leave Faulty Research Grant of Meiji University.

References

- M. Abramovivi, M. A. Breuer and A. D. Friedman, Digital Systems TESTING and Testable DESIGN, pp.43-46, Computer Science Press 1990.
- [2] R. E. Bryant, "Graph-based algorithms for boolean function manipulation," *IEEE Trans. Comput.*, Vol. C-35, no. 8, pp. 677-691, Aug. 1986.
- [3] R. E. Bryant, "Boolean analysis of MOS circuits," IEEE Trans. on CAD of Integrated Circuits, CAD-6(4), pp.634-649, July 1987.
- [4] D. L. Dietmeyer, Logic Design of Digital Systems, Allyn and Bacon, Inc., Boston, 1971.
- [5] G. Jennings, "Symbolic incompletely specified functions for correct evaluation in the presence of indeterminate input values," 28th Hawaii Int'l Conf. on System Science (Vol. I: Architecture), pp.23-31, Jan. 1995.
- [6] S. C. Kleene, Introduction to Metamathematics, Wolters-Noordhoff, North-Holland Publishing, 1952.
- [7] F. Mileto and G. Putzolu, "Average values of quantities appearing in Boolean function minimization," *IEEE Trans. Elec. Comput.*, vol. EC-13, No. 4, pp. 87-92, April 1964.
- [8] S. Minato, "Graph-based representation of discrete functions," in [10].
- [9] T. Sasao (ed.), Logic Synthesis and Optimization, Kluwer Academic Publishers, 1993.
- 10] T. Sasao and M. Fujita (ed.), Representations of Discrete Functions, Kluwer Academic Publishers, 1996.
- [11] T. Sasao, "Ternary decision diagrams and thier applications," in [10].
- [12] T. Sasao, "Optimization of pseudo-Kronecker expressions using multiple-place decision diagrams," *IEICE Trans.* vol. E76-D, No. 5, 1993.