

19th International Workshop on Post-Binary ULSI Systems

May 25, 2010

Casa Convalescència, Barcelona, Spain

Program

9:30 AM - 9:35 AM	<i>Opening Remark</i>	ULSIWS2010 Co-Chair, S. Nagayama (Hiroshima City University)
		Session Chair: N. Nagayama (Hiroshima City University)
9:35 AM - 10:00 AM	<i>PBM128: A Parallel Branching Program Machine Consisting of 128 Branching Program Machines</i>	
		H. Nakahara, T. Sasao, M. Matsuura (Kyushu Institute of Technology), Y. Kawamura (Renesas Technology Corp.)
10:00 AM - 10:25 AM	<i>Synthesis of Threshold Logic Networks from Decision Diagrams</i>	
		M. Stanković, C. Moraga (Centre for Soft Computing)
10:25 AM - 10:50 AM	<i>Array Multiplier Based on Binary Stored Carry-or-Borrow Representation</i>	
		D. Torno (Exorand Technology)
10:50 AM - 11:10 AM	[Coffee Break]	
		Session Chair: Y. Yuminaka (Gunma University)
11:10 AM - 11:35 AM	<i>Process-Variation-Aware VLSI Design Using a Emerging Functional Devices and Its Impact</i>	
		M. Natsui and T. Hanyu (Tohoku University)
11:35 AM - 12:00 PM	<i>Low-Voltage Multiple-Valued CMOS Logic and Applications</i>	
		Y. Berg (University of Oslo)
12:00 PM - 1:05 PM	[Lunch]	
		Session Chair: M. Miller (University of Victoria)
1:05 PM - 1:30 PM	<i>Critical Behavior of Neuromorphic Logic Nanocells for Random Phenomena Implementation</i>	
		A. H. Tran, S. N. Yanushkevich, S. E. Lyshevski, V. P. Shmerko (University of Calgary)
1:30 PM - 1:55 PM	<i>Application of Graphical Compositions - Searching for Semirigid Systems of Equivalence Relations -</i>	
		M. Miyakawa, M. Pouzet, I.G.Rosenberg, H. Tatsumi (Tsukuba University of Technology)
1:55 PM - 2:00 PM	<i>Closing</i>	ULSIWS2010 Co-Chair, S. Nagayama (Hiroshima City University)