

5th CALL FOR PAPERS

Reed-Muller 2009

<http://www.lsi-cad.com/RM>

May 23-24, 2009, Naha, Okinawa, Japan

After the International Symposium on Multiple-Valued Logic,
May 21-23, 2009, Naha, Okinawa, Japan.



In this workshop, we consider various types of representations for various discrete functions.

- AND-EXOR based representations that are simpler than standard AND-OR representations.
- Decision diagrams for synthesis, analyses, and verification.
- Spectrum transformation to detect the properties of logic functions.
- Previous workshops have been held in Oslo (Norway) in 2007, in Tokyo (Japan) in 2005, in Trier (Germany) in 2003, in Starkville, Mississippi (USA) in 2001, in Victoria (Canada) in 1999, in Oxford (UK) in 1997, in Chiba (Japan) in 1995, and in Hamburg (Germany) in 1993.
- A non-restrictive list of interests includes the following topics:
 - Graph-based representations of logic functions: BDD, MDD, BMD, EVBDD etc.
 - Spectral representation of logic functions
 - Representations for quantum computing, nano-technology, and molecular scale computing
 - Implementation in silicon (FPLDs, FPGAs, ...)
 - Graph functions, bent functions, and cryptographic applications
 - EXOR-based logic representations
 - Applications, including circuit design, reversible logic, quantum logic, etc.
- Authors are invited to submit extended abstracts or full papers not exceeding 10 two-column format pages to Prof. Jon T. Butler,
Department of Electrical and Computer Engineering, Naval Postgraduate School, Code EC/Bu
Monterey, CA 93943-5121 U.S.A.,
jon_butler@msn.com
- Submissions should be sent by e-mail as an attached PDF (preferred) or postscript file. Please do not submit papers by regular mail or fax.
- **Important Dates**
 - **Extended abstract / draft paper submission: February 1, 2009 (Su)**
 - **Notification of acceptance: March 24, 2009 (Tue)**
 - **Camera Ready Paper Due: April 17, 2009 (Fri)**
 - **Workshop: May 23-24, 2009**
- PC Committee
 - Jon T. Butler, Rolf Drechsler, Gerhard W. Dueck, D. Michael Miller
 - Marek Perkowski, Tsutomu Sasao, Mitchell A. Thornton
- We are planning to publish a book in 2010 from Morgan & Claypool. Selected authors are asked to revise the paper to form a chapter of the book. For Questions: Tsutomu Sasao, Department of Computer Science and Electronics Kyushu Institute of Technology, Iizuka 820-8502, Japan, sasao@cse.kyutech.ac.jp